

FIG. 1 (Prior Art)

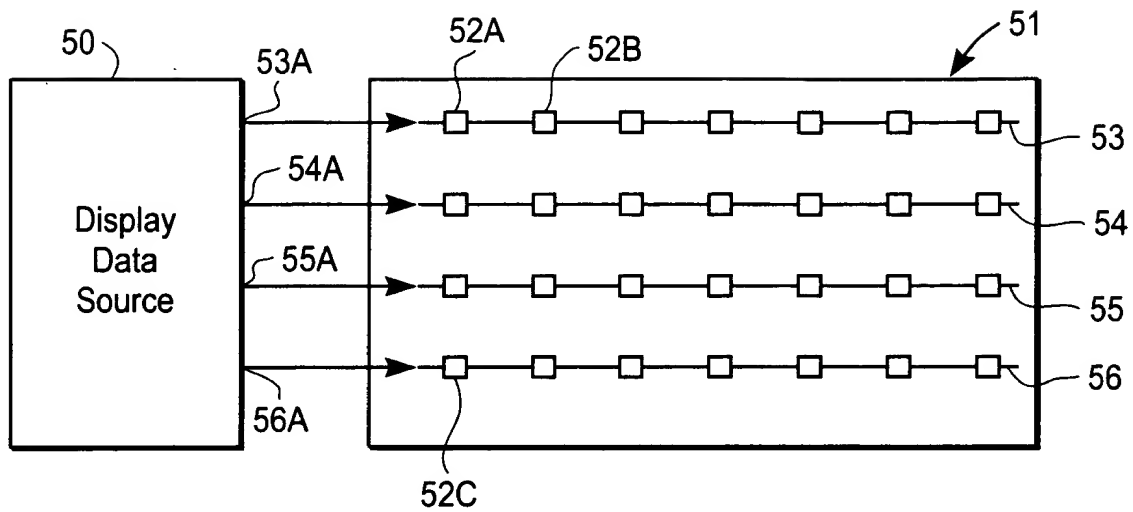


FIG. 2A

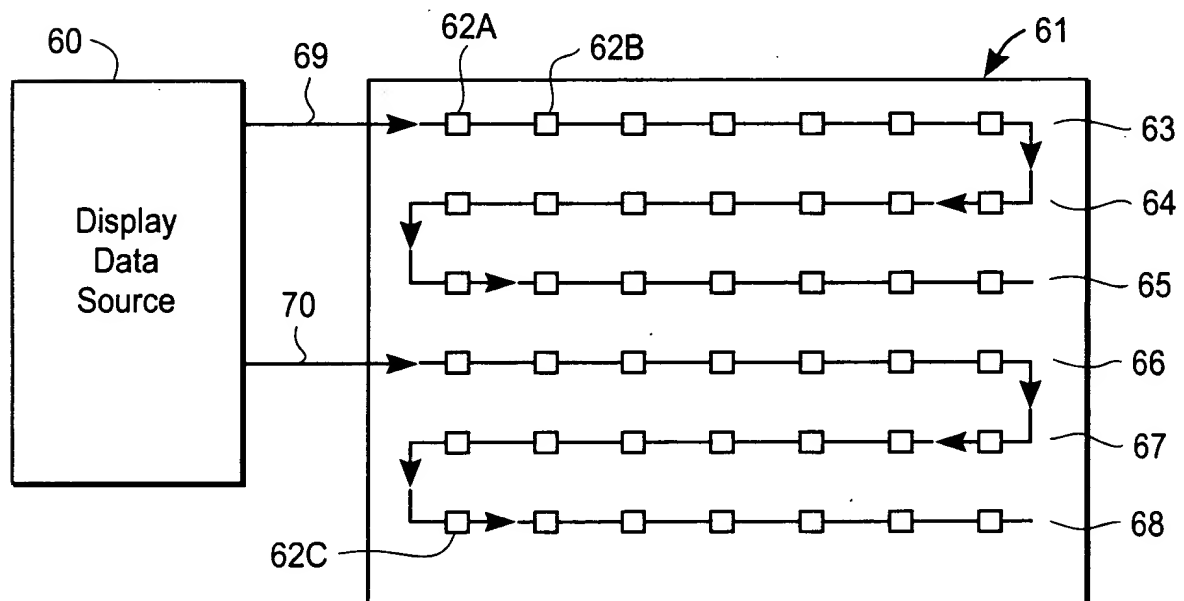


FIG. 2B

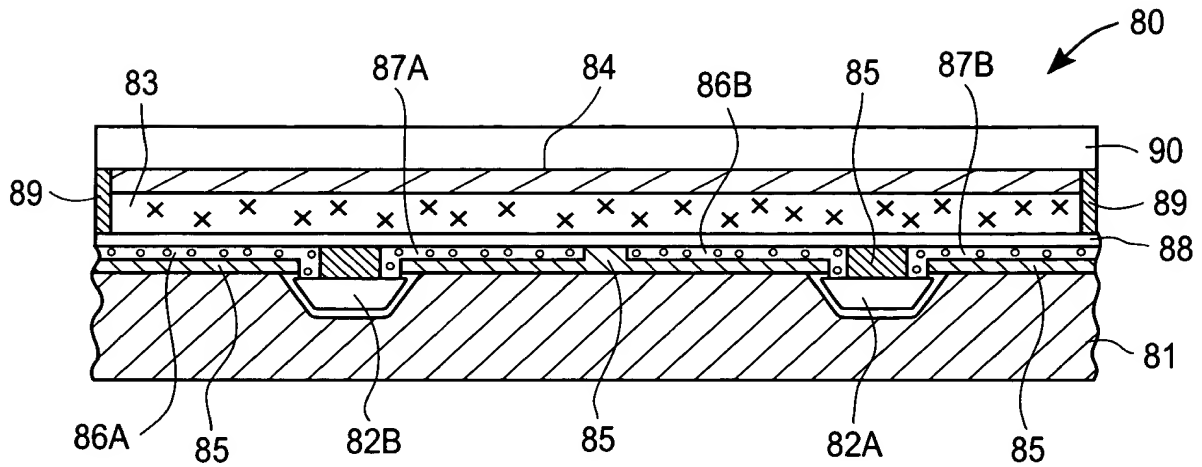


FIG. 2C

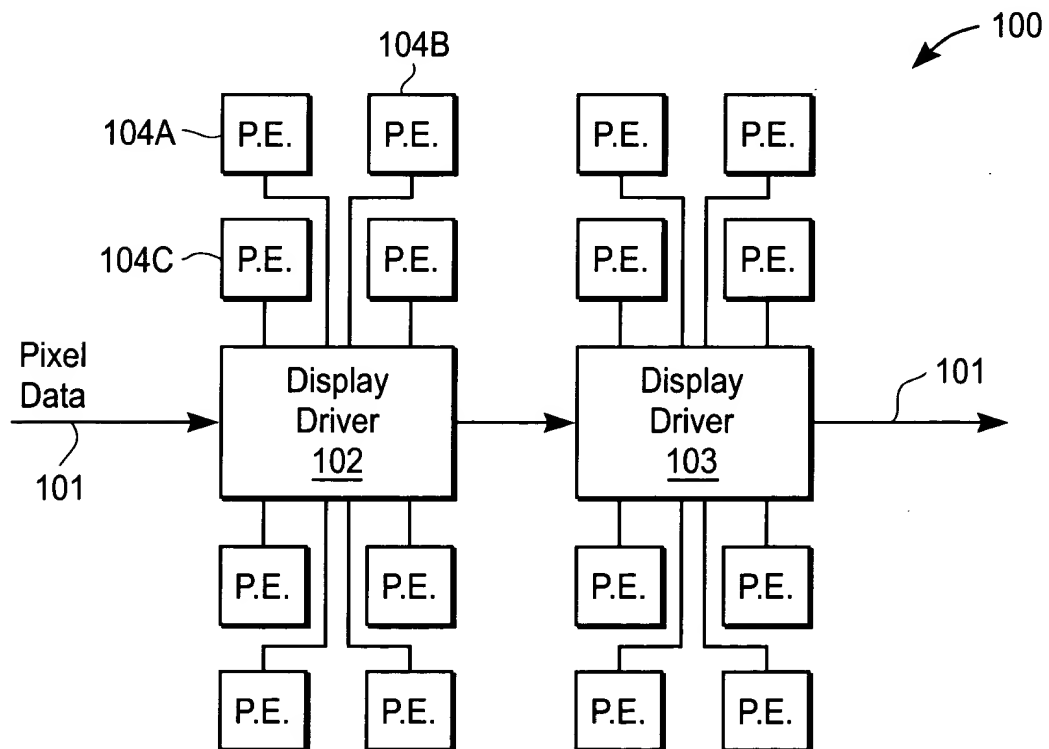


FIG. 3A

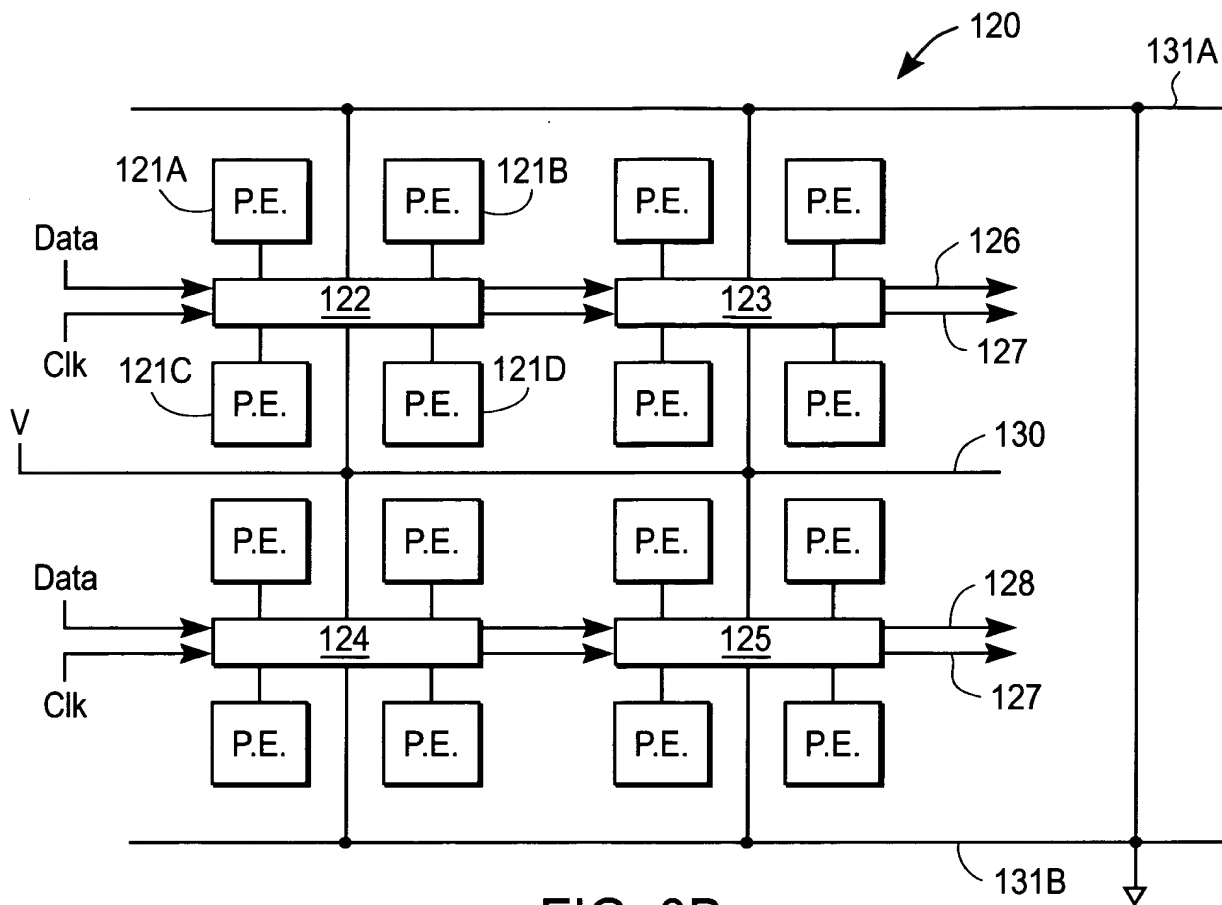


FIG. 3B

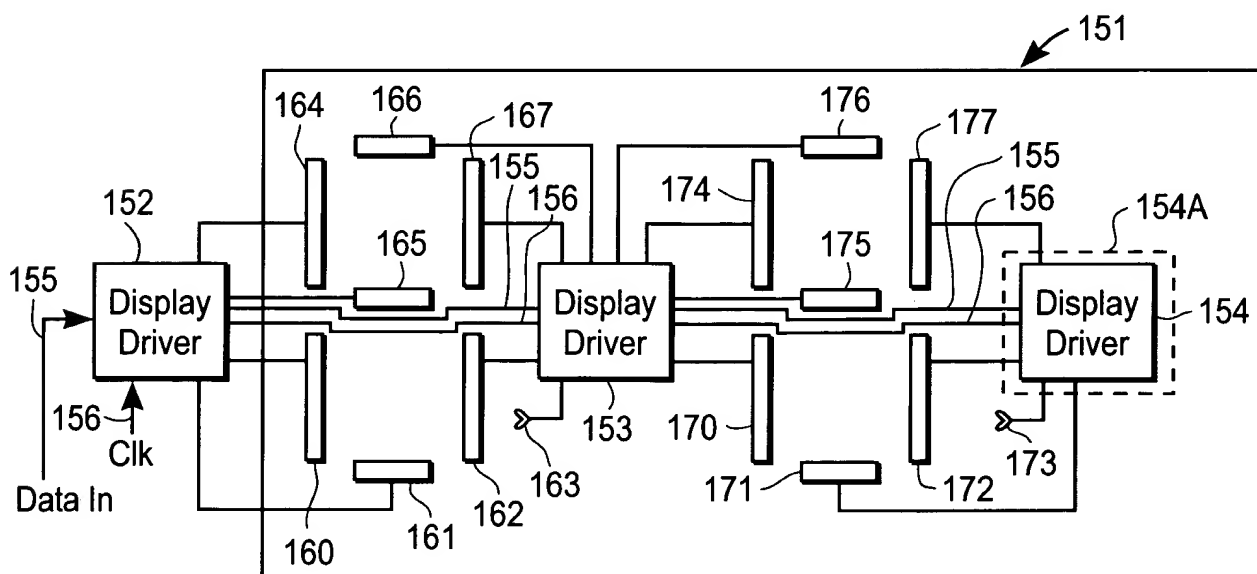


FIG. 3C



**FIG. 3D**

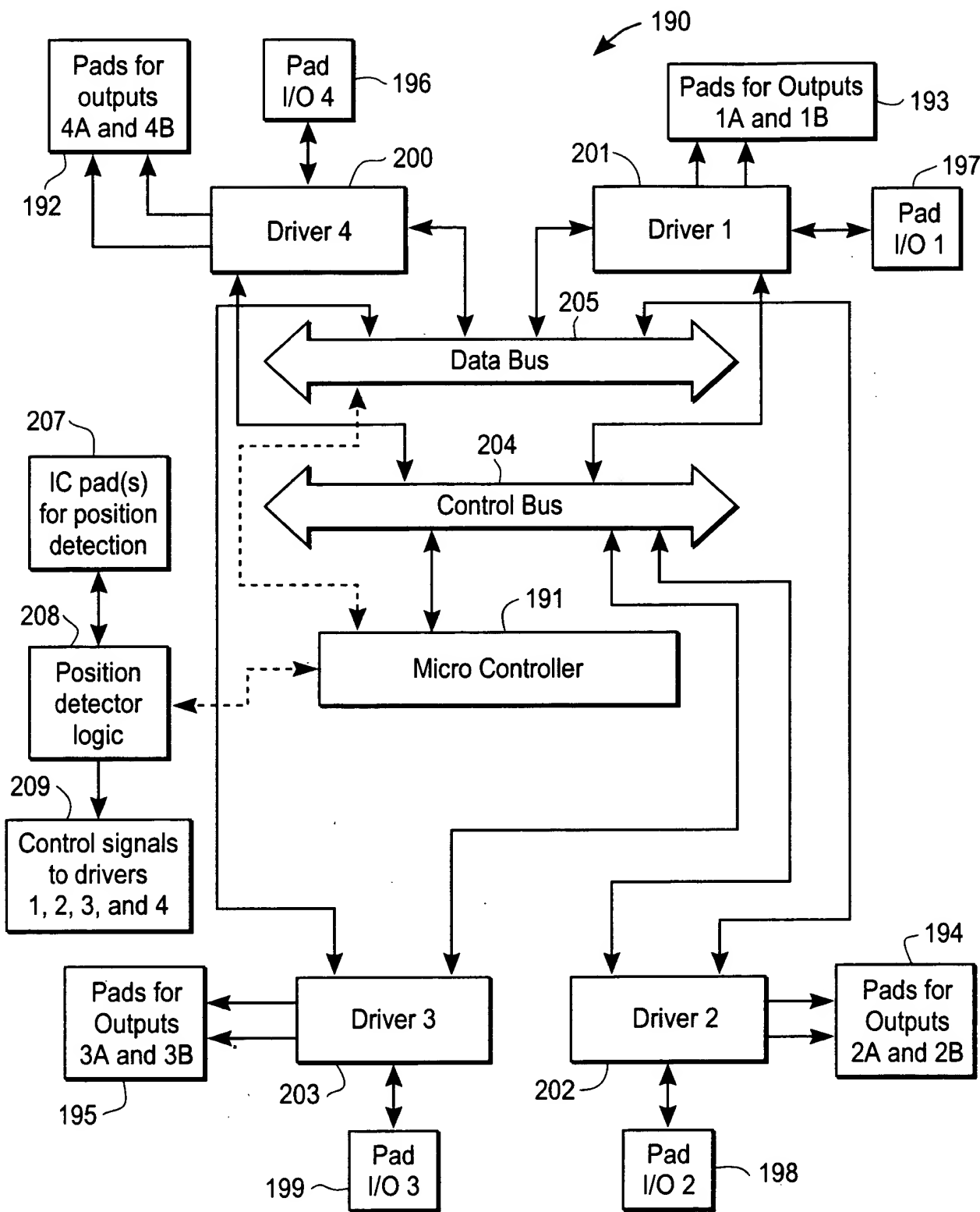


FIG. 4A

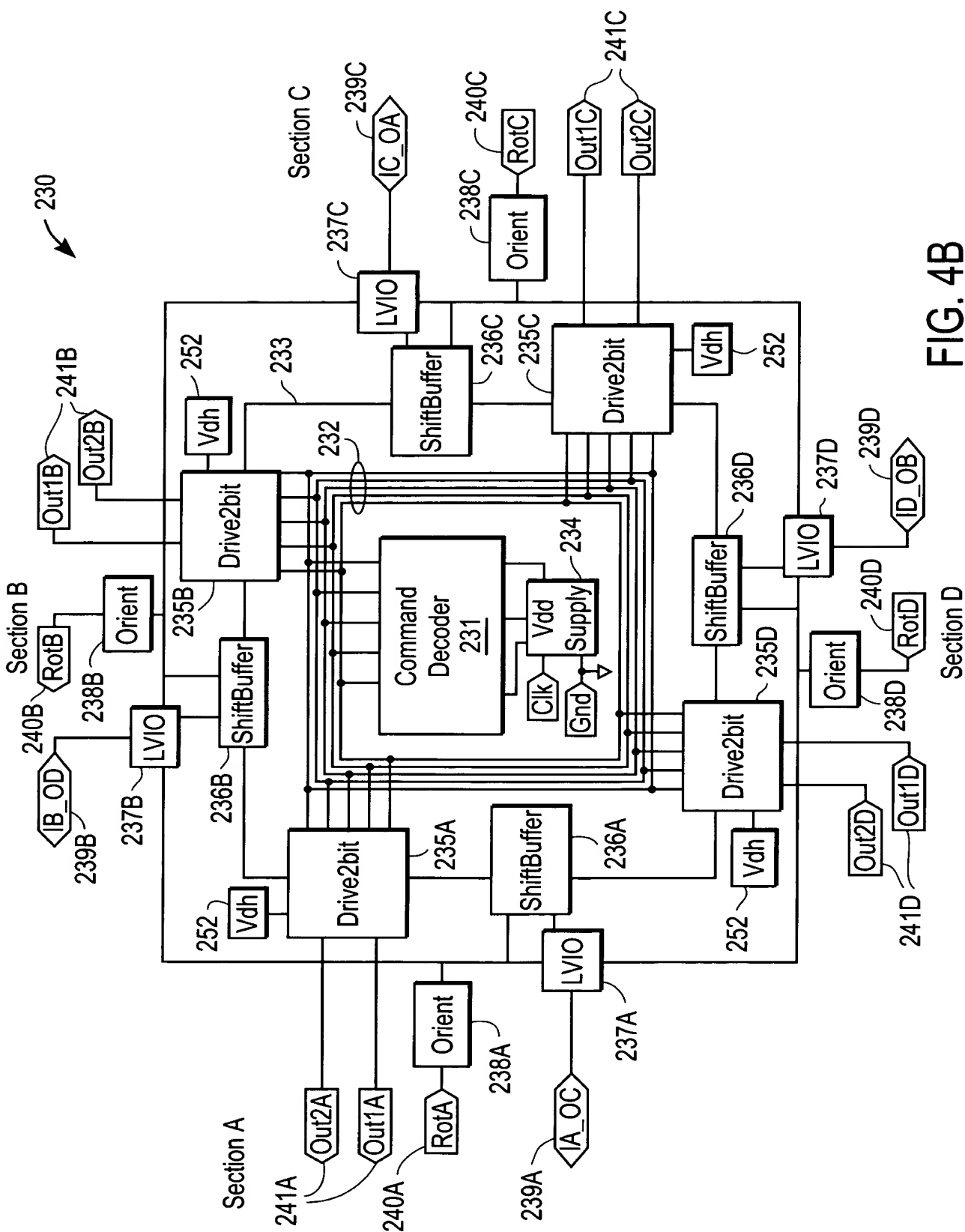


FIG. 4B

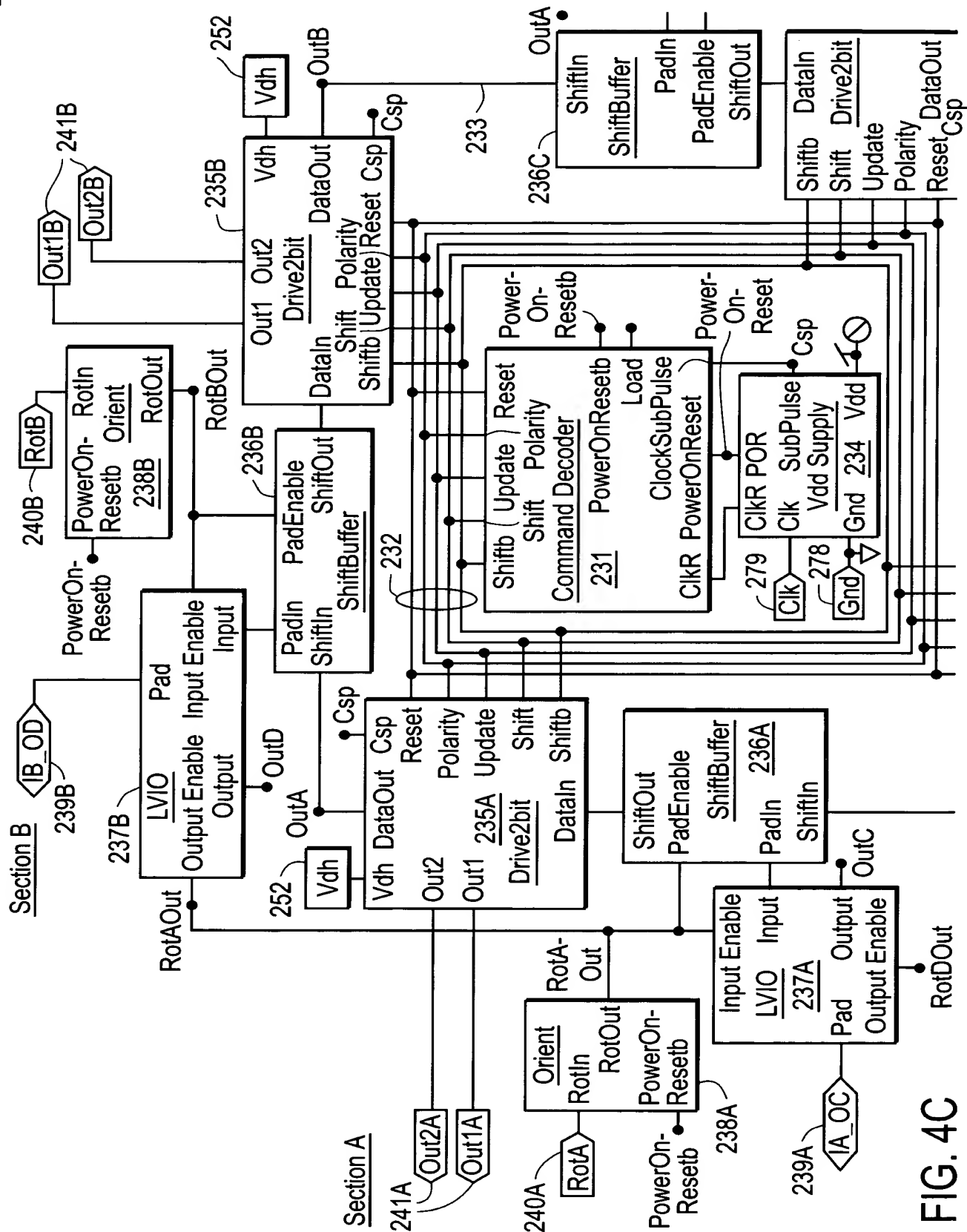


FIG. 4C



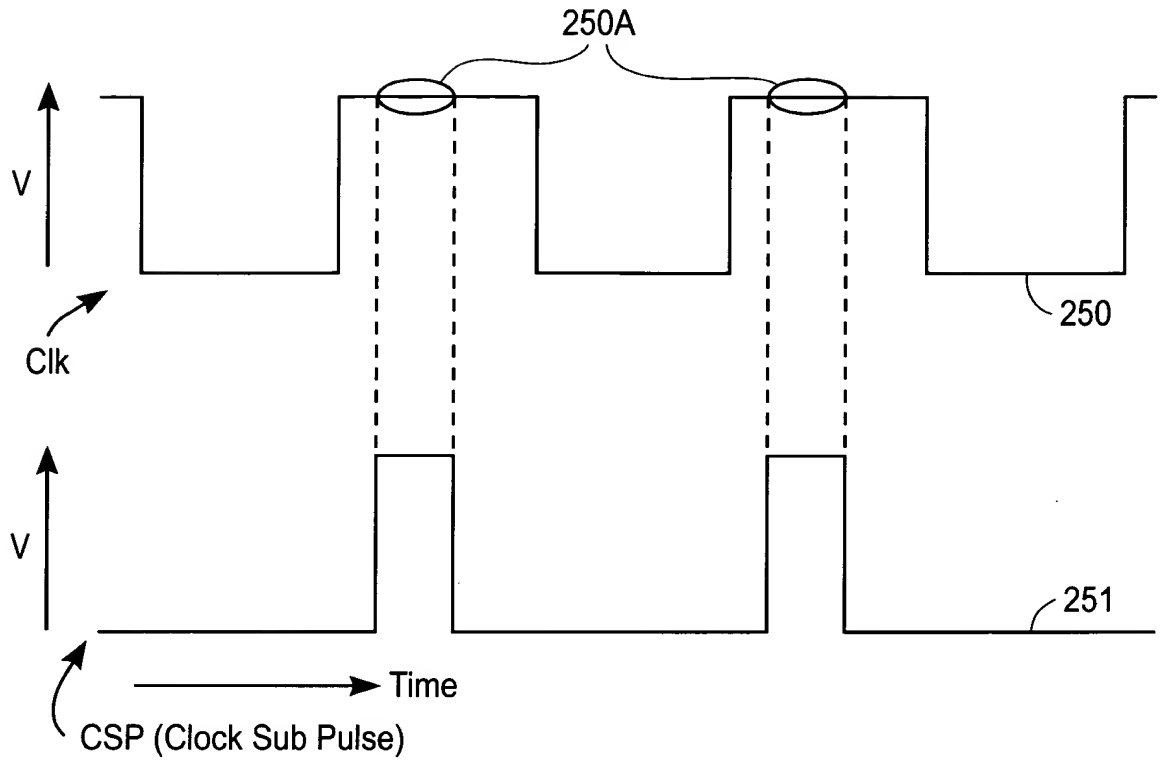


FIG. 5A

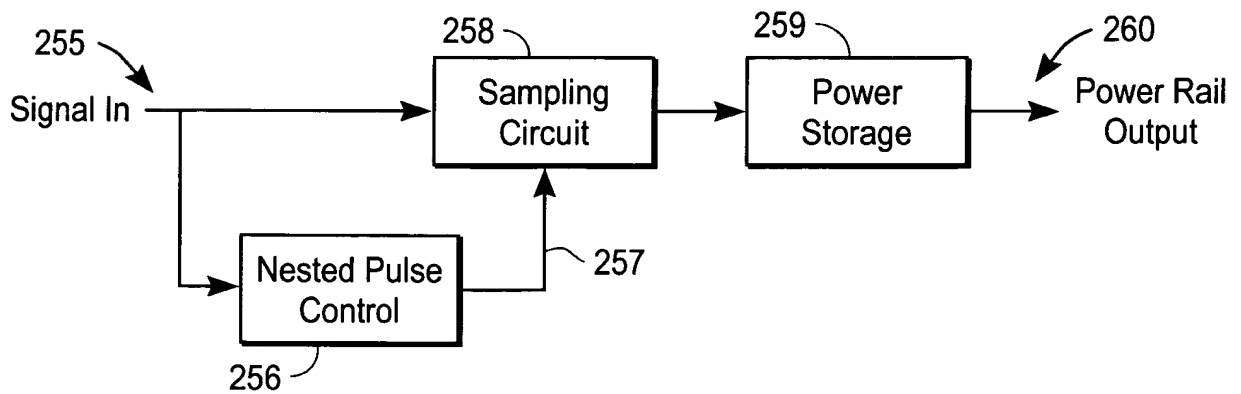


FIG. 5B

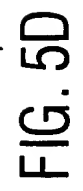


FIG. 5C

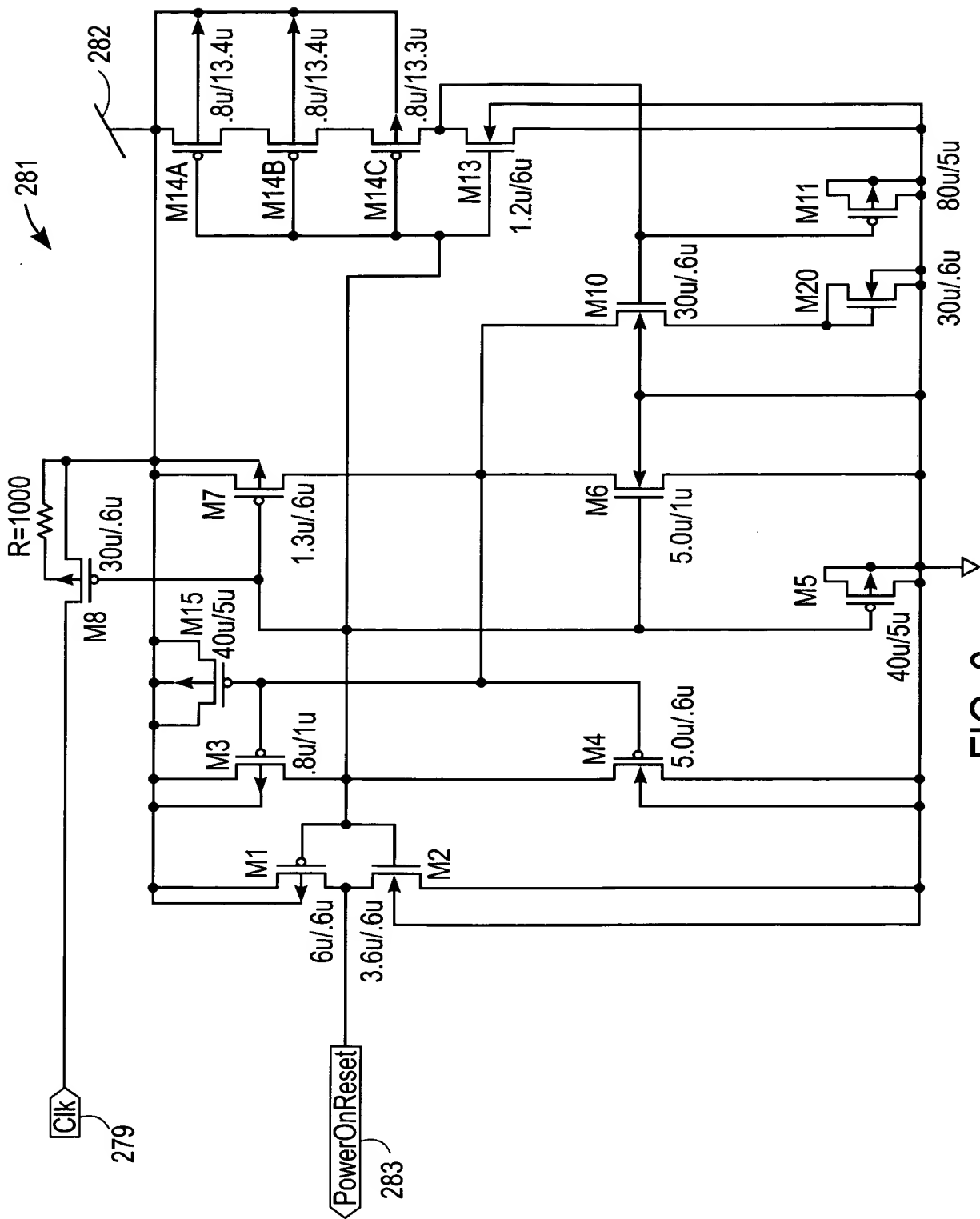


FIG. 6



FIG. 7A

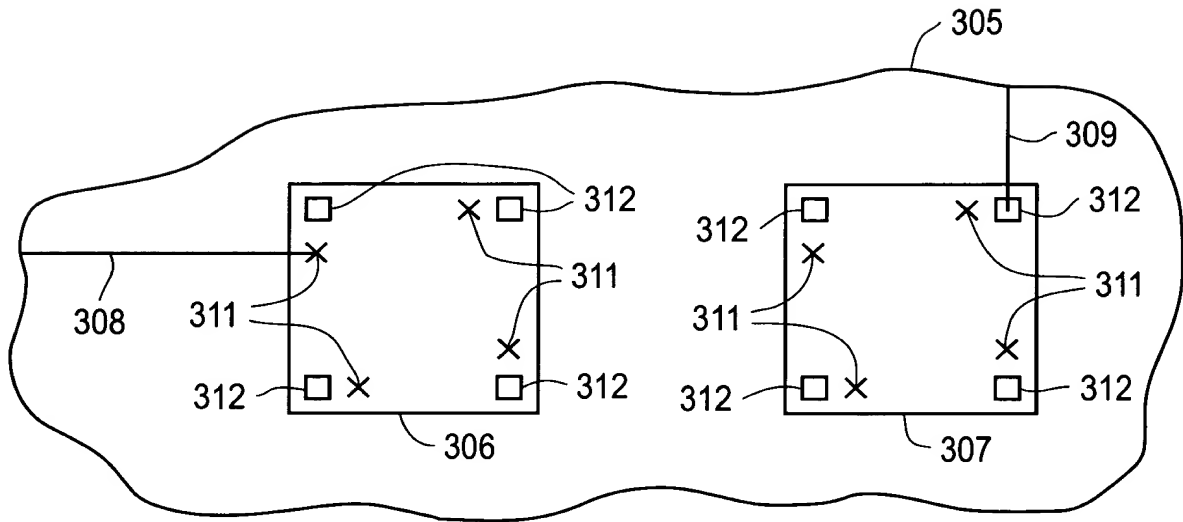


FIG. 7B

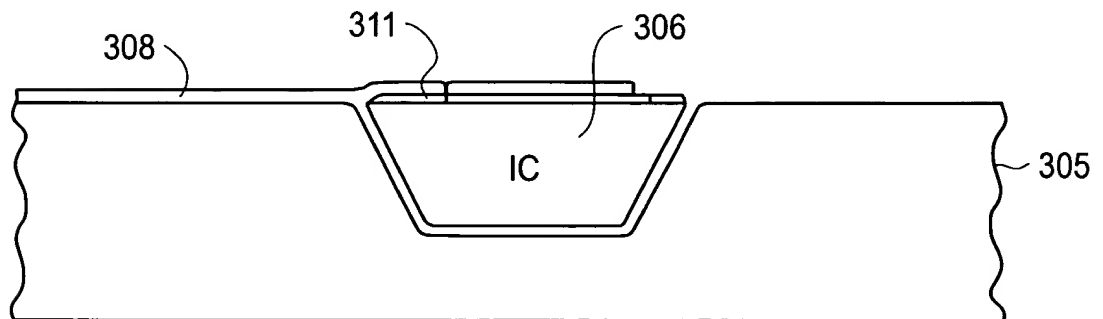


FIG. 7C

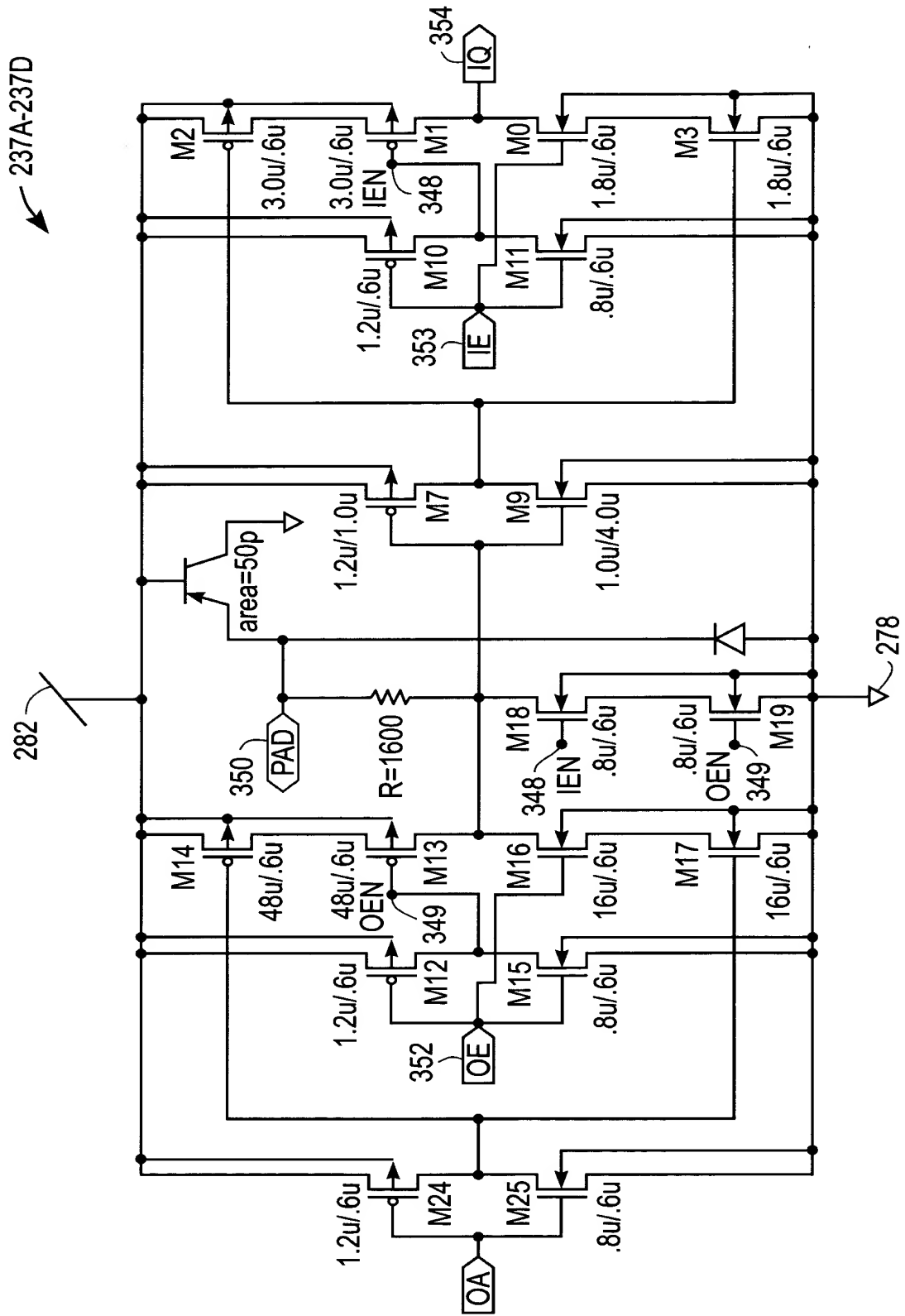


FIG. 8

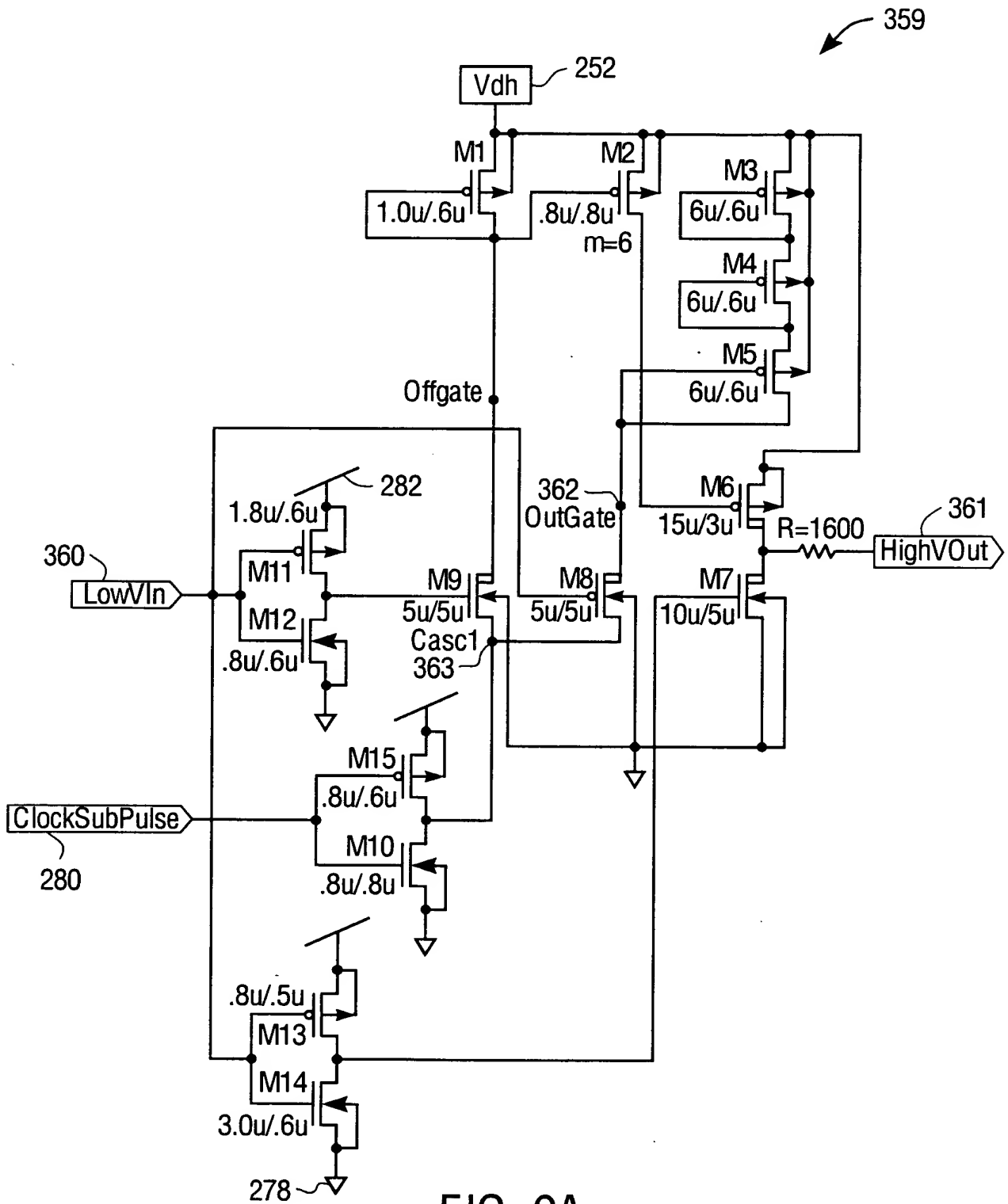


FIG. 9A

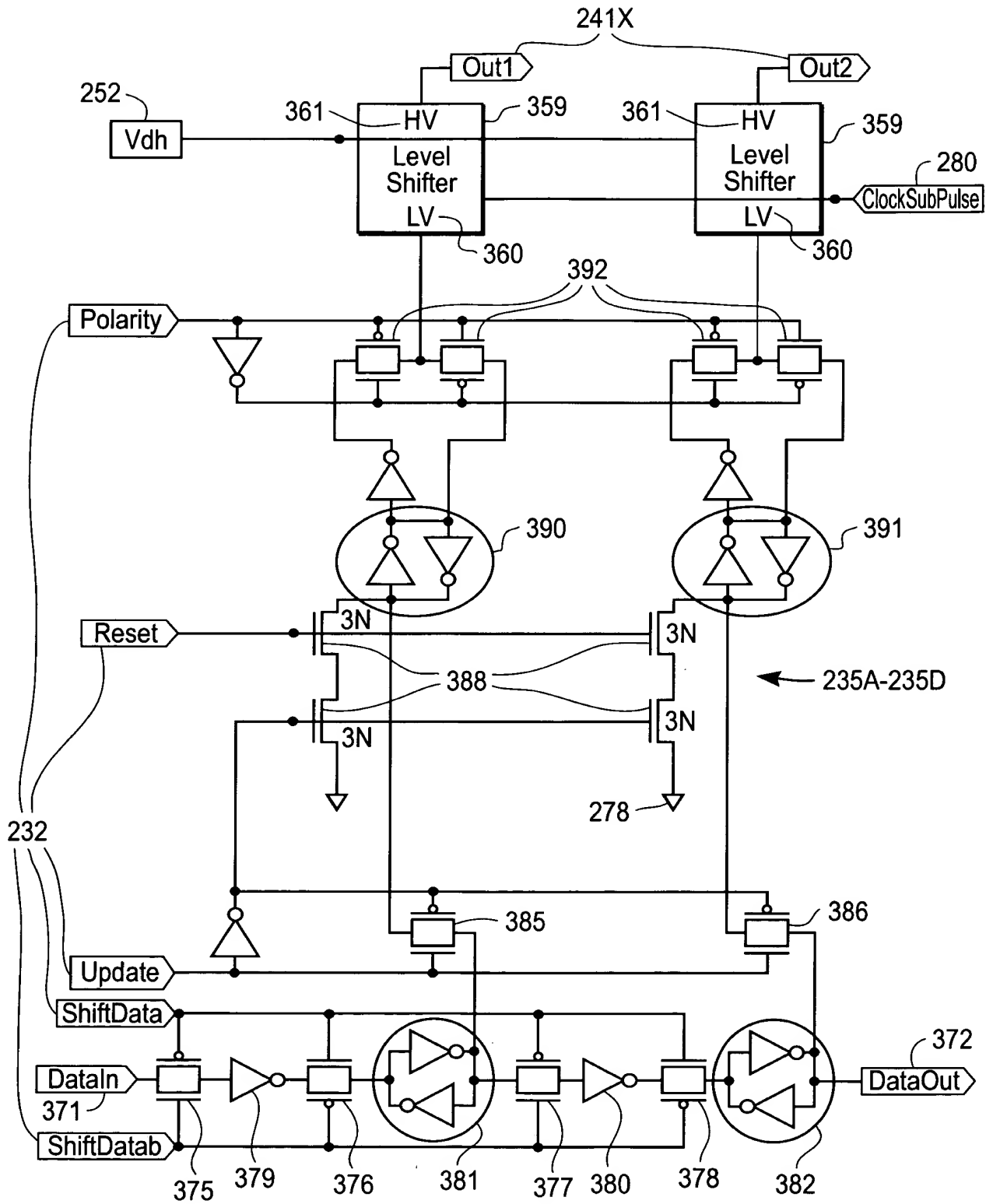
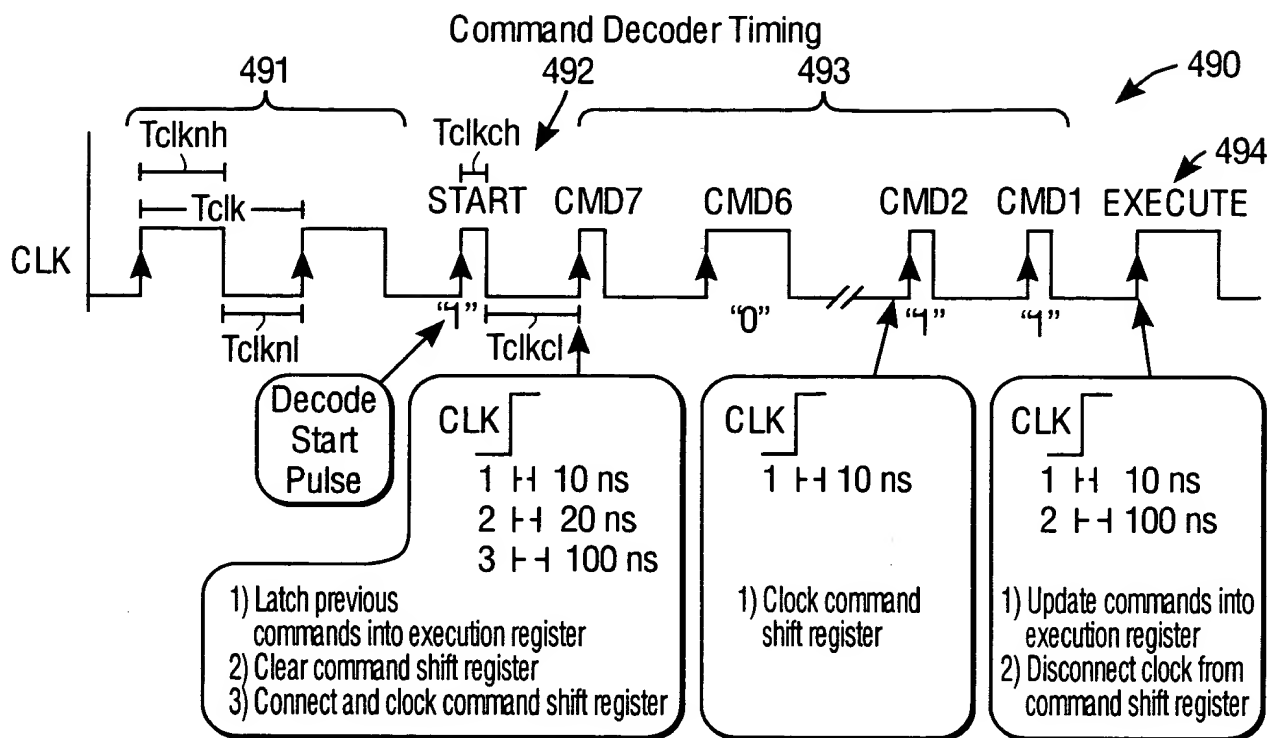


FIG. 10

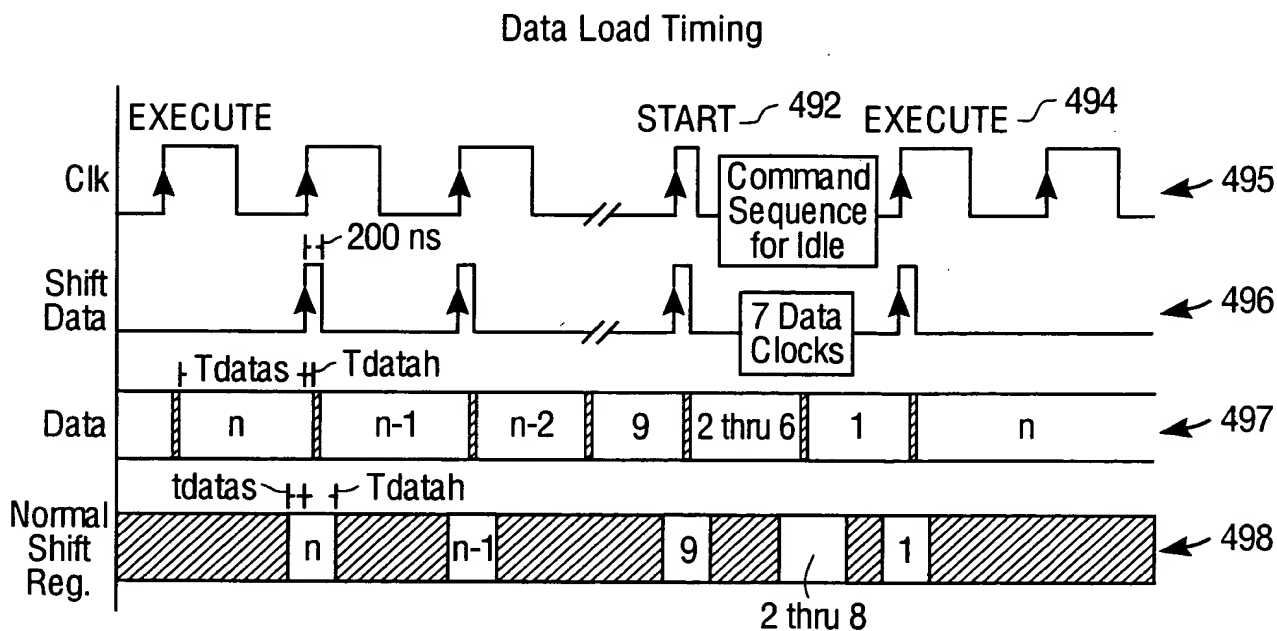




FIG. 11A Command Decoder Circuit



**FIG. 11B**



- n is the total number of data values for all daisy-chained device blocks. (example: n = 32 for 4 device blocks).
- data must be low upon start-up and through the first command sequence.

**FIG. 11C**

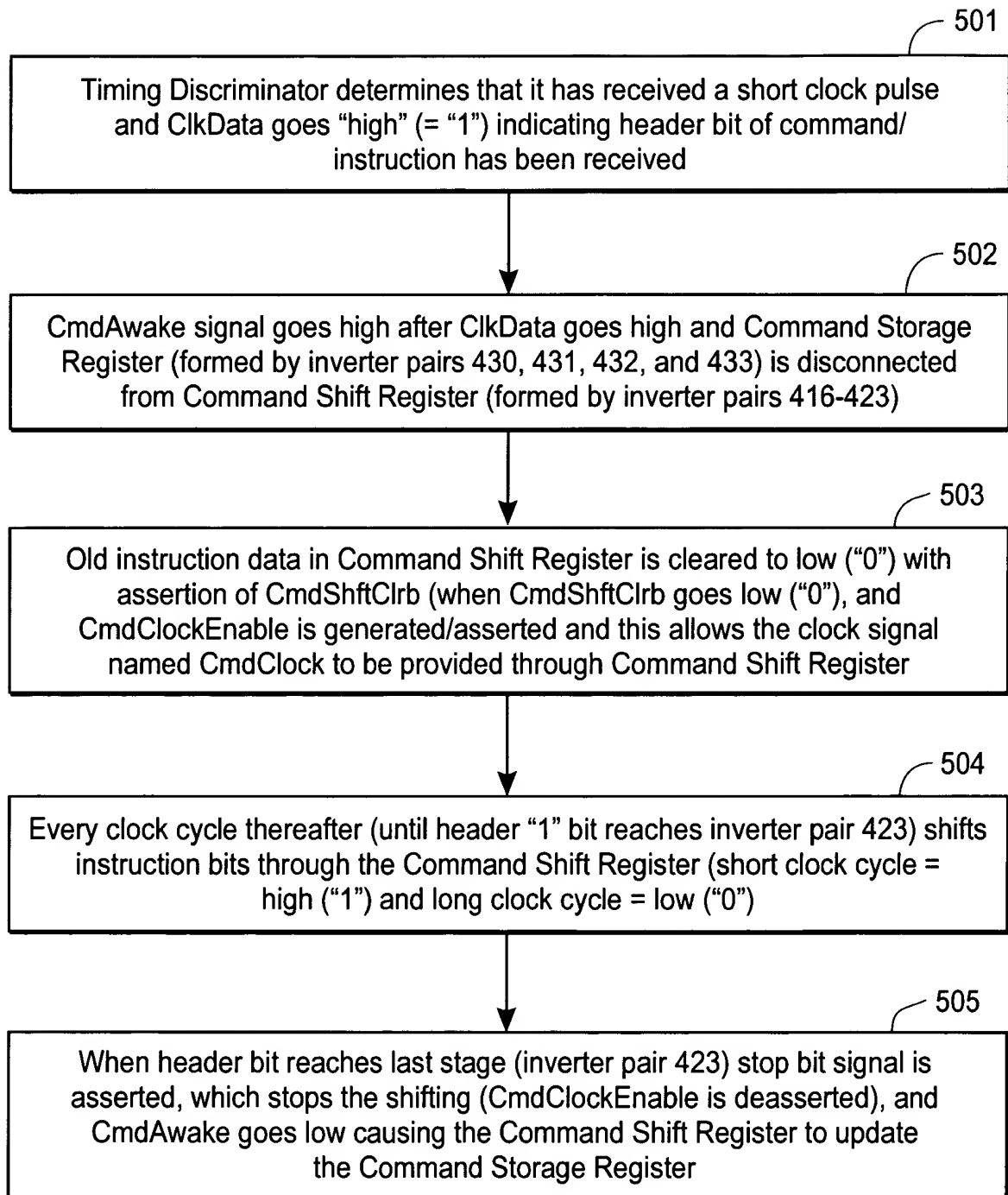


FIG. 11D

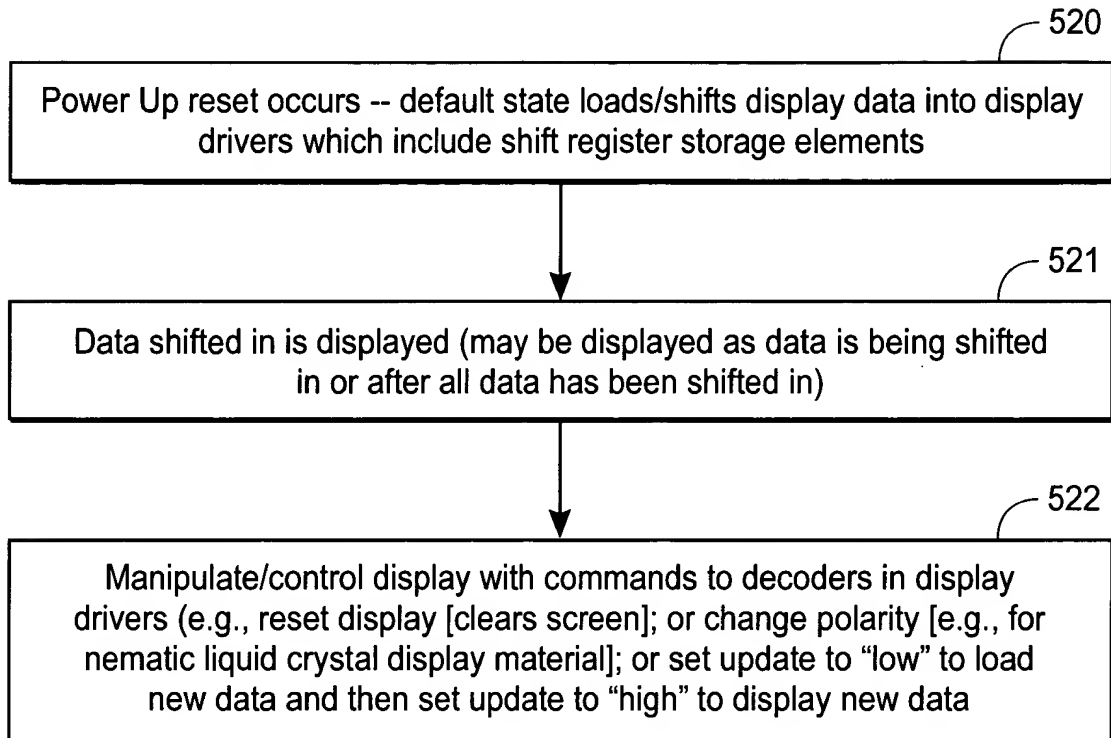


FIG. 11E

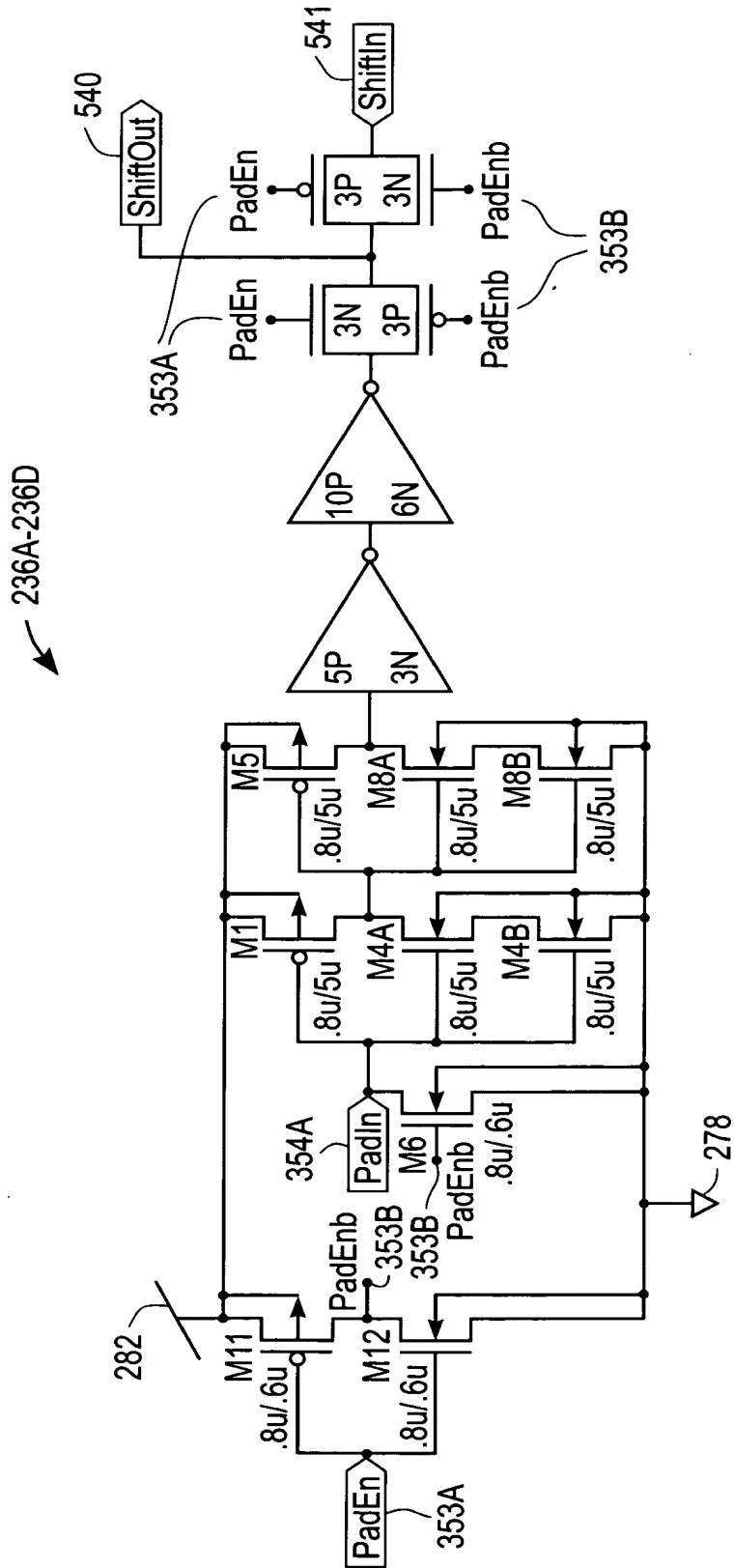


FIG. 12A

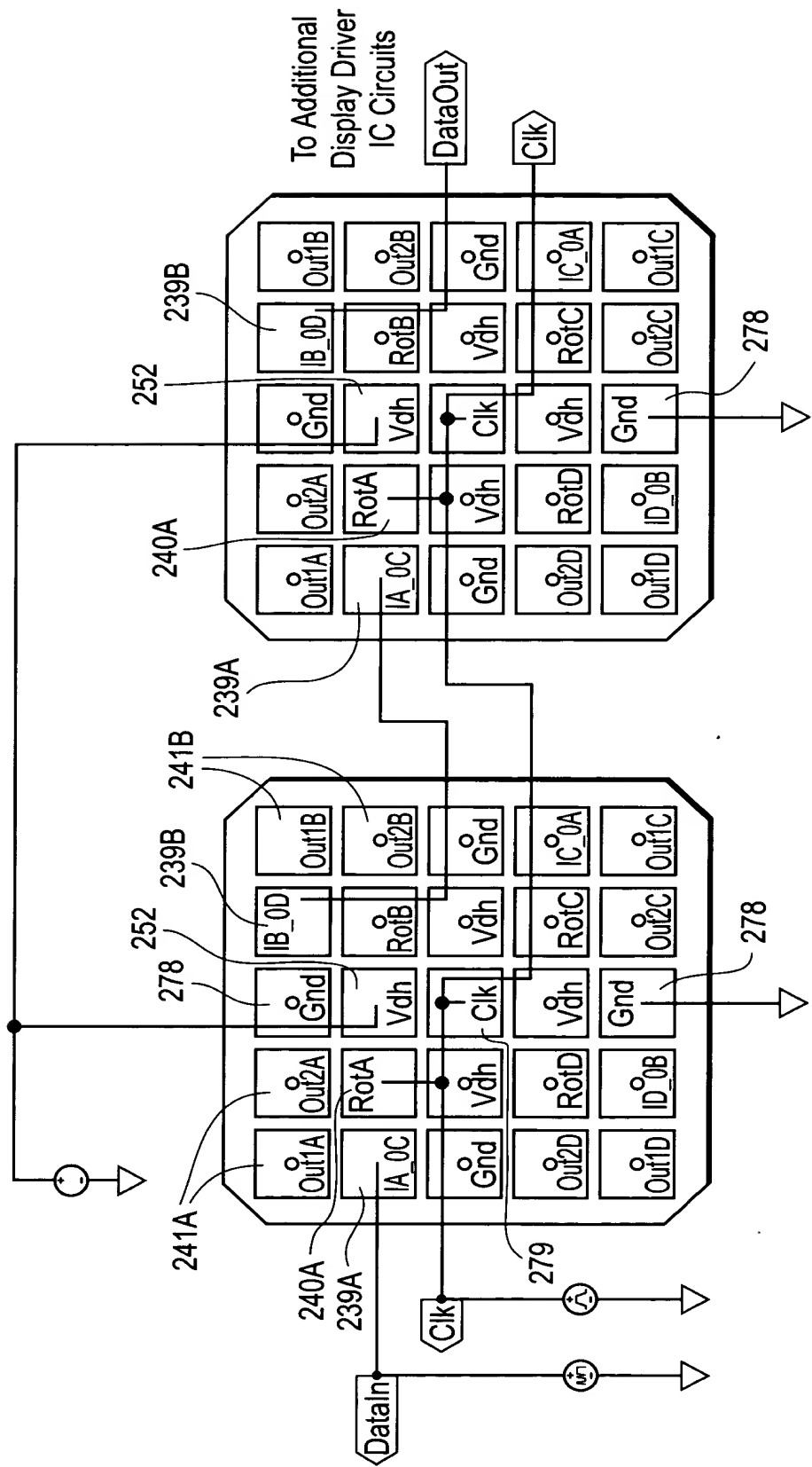


FIG. 13A

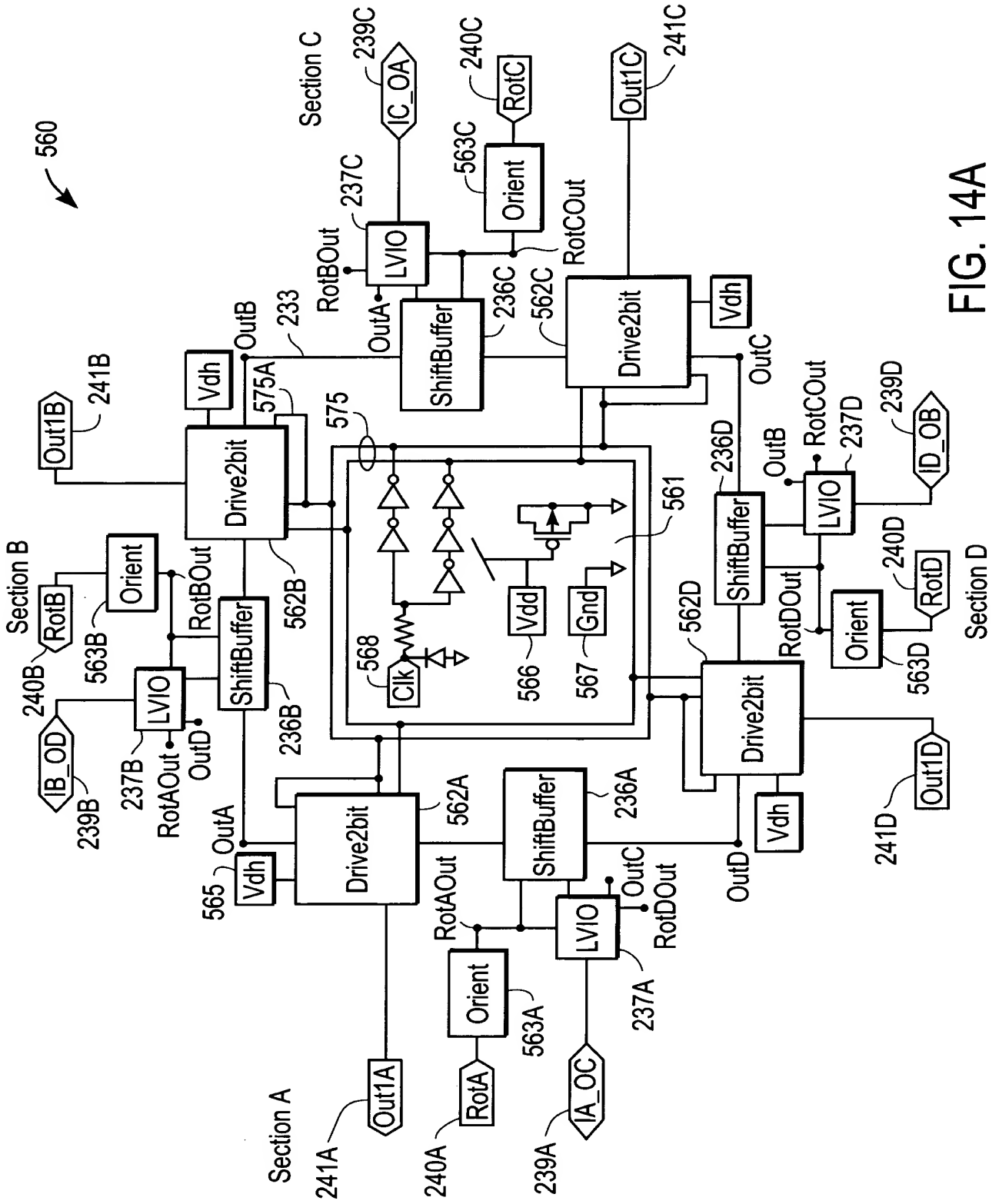


FIG. 14A

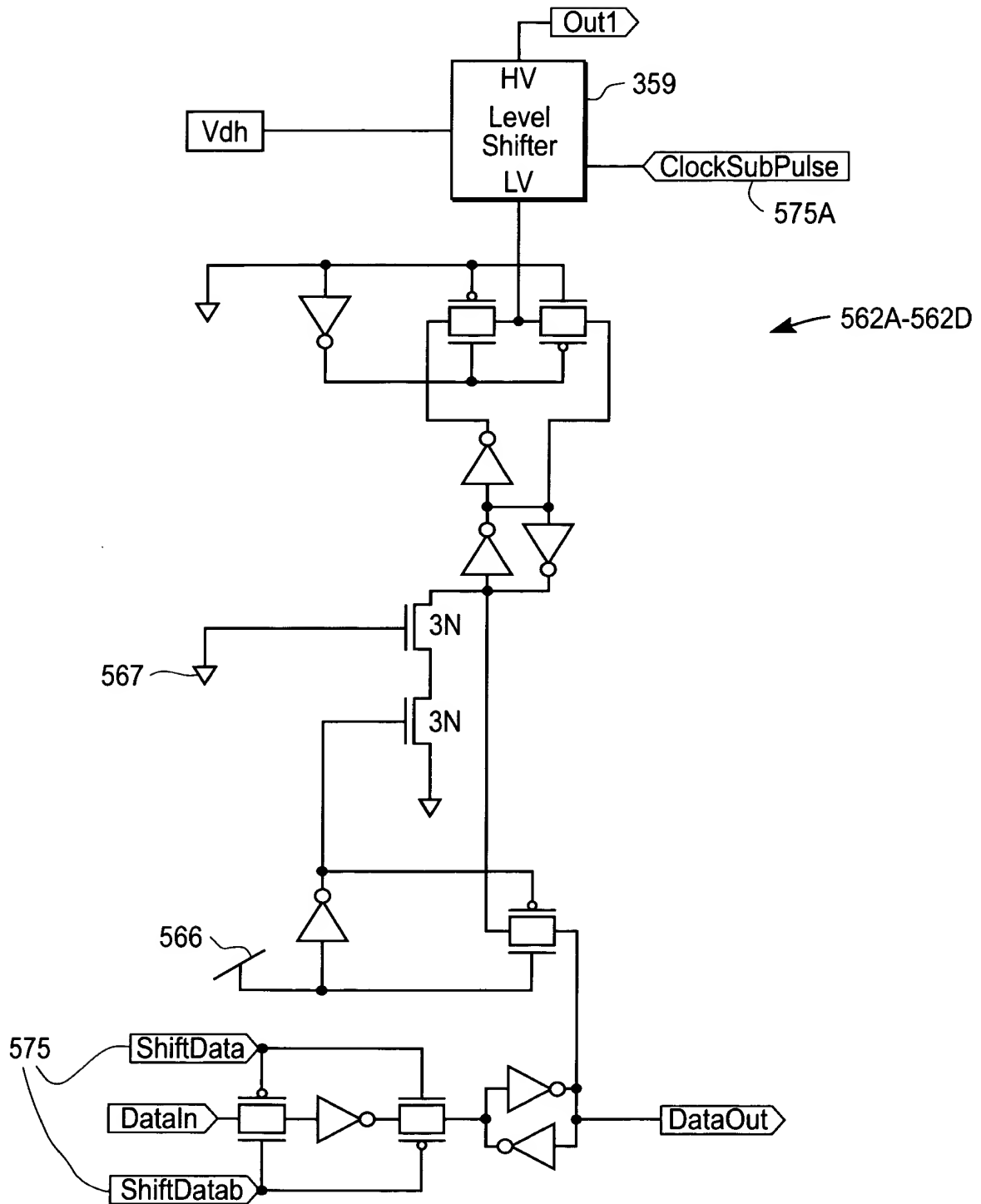


FIG. 14B



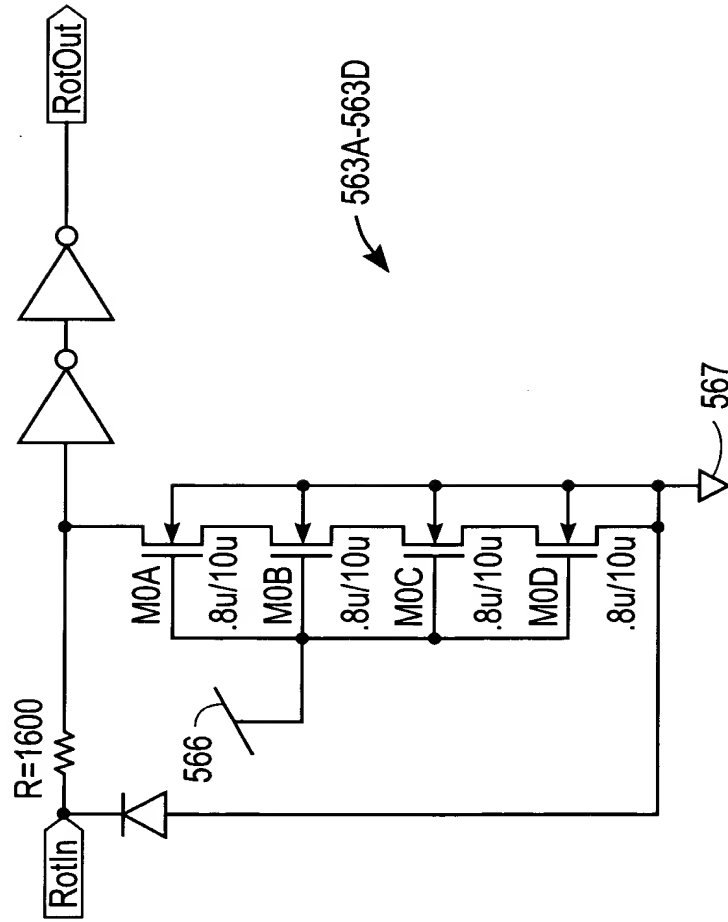


FIG. 14C

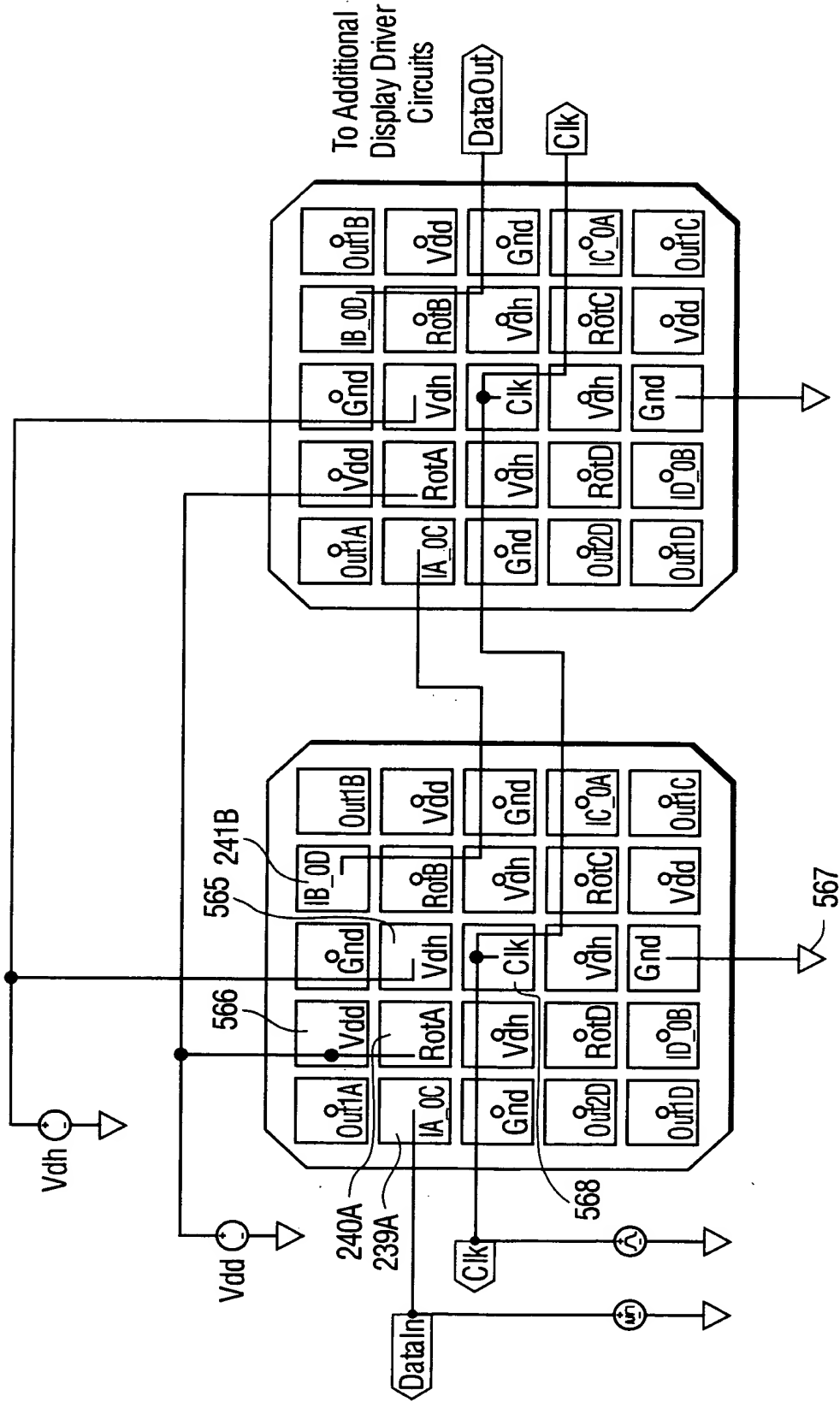
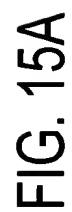


FIG. 14D



**FIG. 15A**

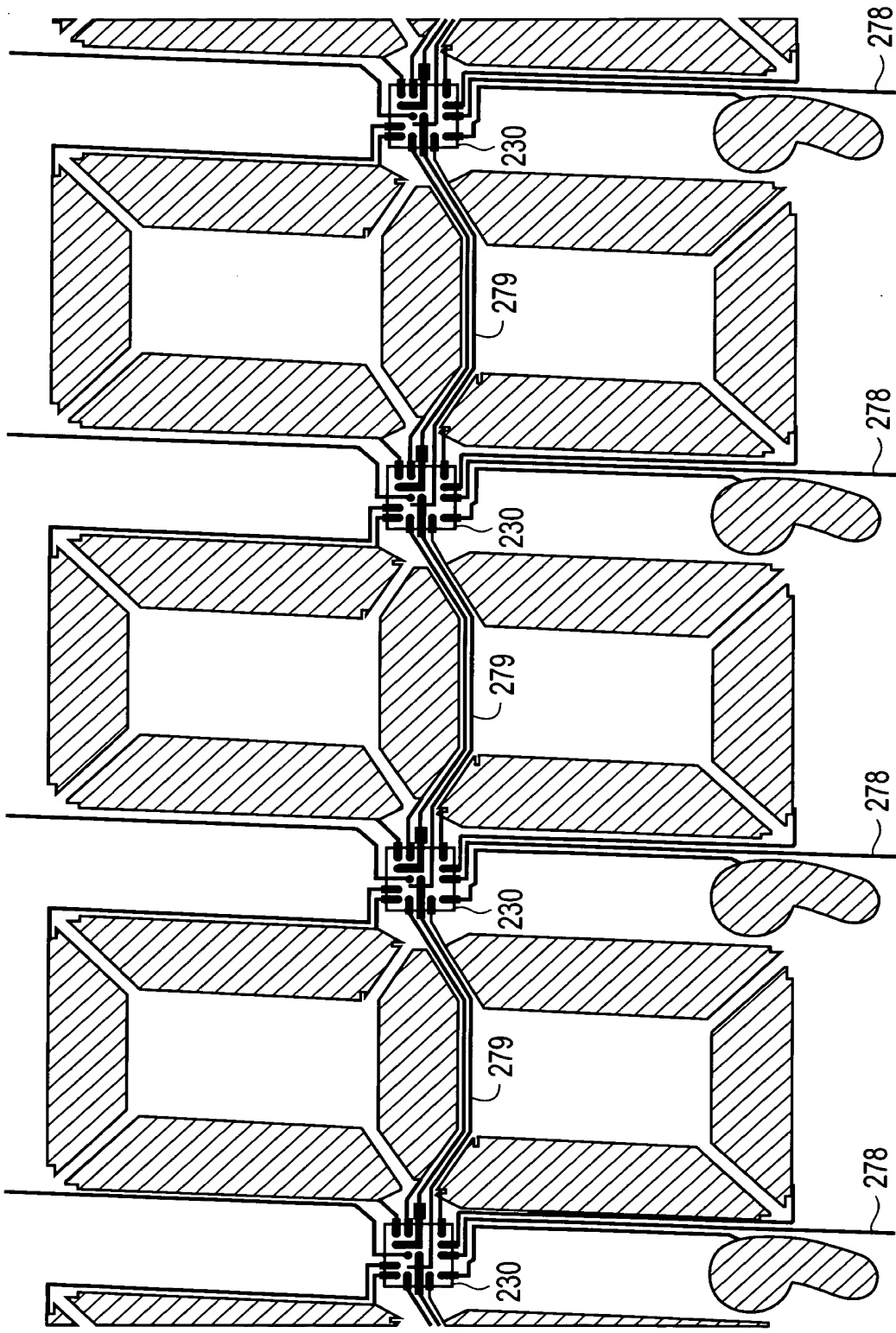


FIG. 15B